

Serial No.: 10/051,726  
Eclipse Group: ST01015USU (133-US-U1)

**AMENDMENTS TO THE SPECIFICATION**

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Please replace the paragraph on page 8, lines 1-4 with the following:

--When V2 112 is connected to the source of Q1 202 as shown in FIG. 2, current flow from V2 ~~[[202]]~~ 212 can be prevented by lowering the gate voltage present at Q1 202 to a point where  $V_{gs}$  exceeds the necessary threshold for a pinch-off condition to occur. Thus, when the desired voltage source V1 110 is present, it is desirable to have the gate of Q1 202 at zero volts (0V). --